

10/581786

1A P20 Rec'd PCT/PTO 06 JUN 2006

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

GEVORGIAN ET AL.

Atty. Ref.: 3670-66

Serial No. Unknown

TC/A.U.: Unknown

Filed: June 6, 2006

Examiner: Unknown

For: A DELAY LOCKED LOOP WITH PRECISION CONTROLLED DELAY

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June 6, 2006

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

INFORMATION DISCLOSURE STATEMENT

As suggested by 37 C.F.R. 1.97, the undersigned attorney brings to the attention of the Patent and Trademark Office the references listed on the attached form PTO/SB/08a. A copy of each listed foreign patent document and article is attached.

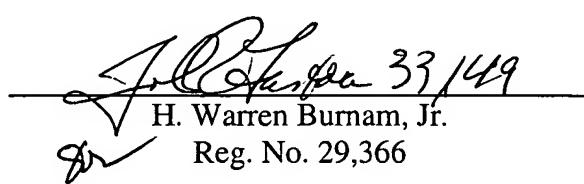
This is not to be construed as a representation that a search has been made or that no better prior art exists, or that a reference is relevant merely because cited.

The Examiner is requested to initial the attached form PTO/SB/08a and to return a copy of the initialed document to the undersigned as an indication that the attached references have been considered and made of record.

Respectfully submitted,

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By:


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Sheet 1 of 1

INFORMATION DISCLOSURE CITATION	ATTY. DOCKET NO.	SERIAL NO.
	3670-66	Unknown 0/581786
	APPLICANT	
	GEVORGIAN ET AL.	
(Use several sheets if necessary)	FILING DATE	TC/A.U.
	June 6, 2006	Unknown

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.)

International Search Report for PCT/SE2003/001918 dated 2 August 2004.
Eto et al., A 333MHz, 20mW, 18ps resolution digital DLL using current-controlled delay with parallel variable resistor DAC (PVR-DAC), Proceedings of the Second IEEE Asia Pacific Conference on ASICS, 2000, pp. 349-350.

*Examiner

Date Considered

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to application.